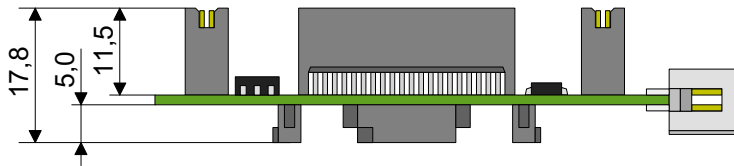


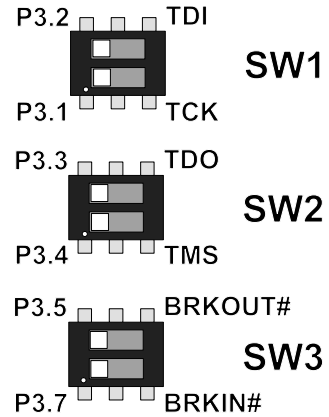
**Quad Connector Converter Adapter for Targets with Quad Connector QC64T**  
Adapter QC144 - QC64T XC164CM  
Part No. DZS-XC164CM-T64-QC

Top Connection :           QC144 for DProbeXC  
Bottom Connection :       QC64T for Infineon XC164CM TQFP-64

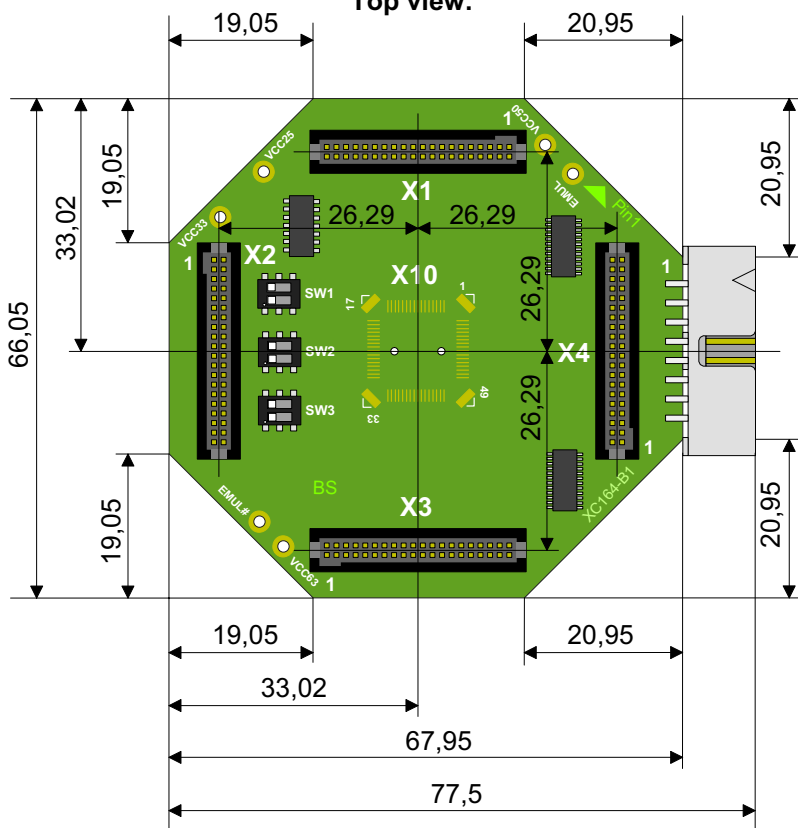
**Side view:**



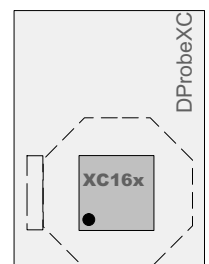
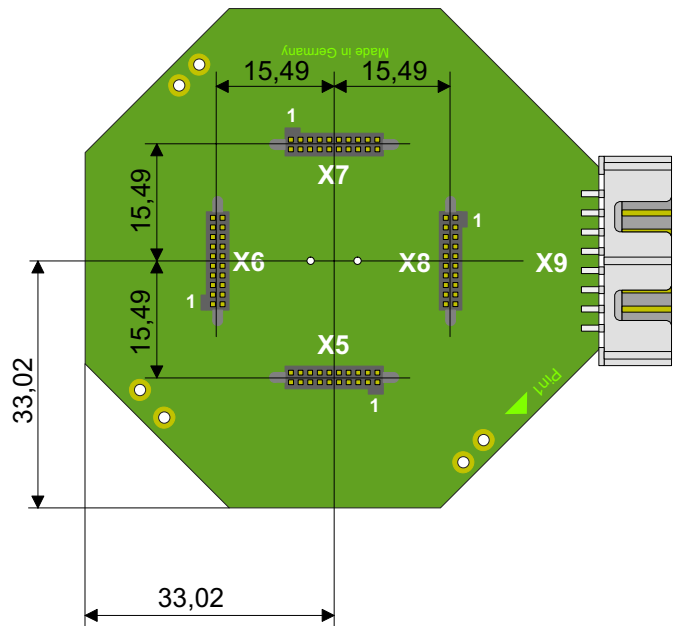
**Dip Switch settings:**



**Top view:**



**Bottom view:**



<p><b>Jumper-Settings for the DProbe 167- C164CS Configurator</b>  <b>Adapter QC144-QC100T for XC164CS</b>  <b>Part-No. DZS-XC164-T100-QC</b></p>
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On the XC164 in 100 pin package, 4 JTAG pins and the two break pins are overlaid on GPIO pins. Using the jumpers listed below the configurator can be configured to connect the necessary JTAG signals and break pins for the use of a JTAG debugger by connecting the dedicated pins of the target processor to the the 16pin JTAG connector.

<b>DIP Switch</b>	<b>Position</b>	<b>XC164 Pin</b>	<b>Usage</b>
<b>SW1-1(TCK)</b>	PORT	P3.1	P3.1 as I/O or T6OUT Or RxD1
	JTAG	TCK	JTAG (connected to TCK on 16pin JTAG connector)
<b>SW1-2(TDI)</b>	PORT	P3.2	P3.2 as I/O or CAPIN
	JTAG	TDI	JTAG (connected to TDI on 16pin JTAG connector)
<b>SW2-1(TDO)</b>	PORT	P3.3	P3.3 as I/O or T3OUT
	JTAG	TDO	JTAG (connected to TDO on 16pin JTAG connector)
<b>SW2-2(TMS)</b>	PORT	P3.4	P3.4 as I/O or T3EUD
	JTAG	TMS	JTAG (connected to TMS on 16pin JTAG connector)
<b>SW3-1(BRKIN#)</b>	PORT	P3.5	P3.5 as I/O or T4IN or TxD1
	BRK	BRKIN#	JTAG (connected to BRKIN# on 16pin JTAG connector)
<b>SW3-2(BRKOUT#)</b>	PORT	P3.6	P3.6 as I/O or T2IN
	BRK	BRKOUT#	JTAG (connected to BRKOUT# on 16pin JTAG connector)