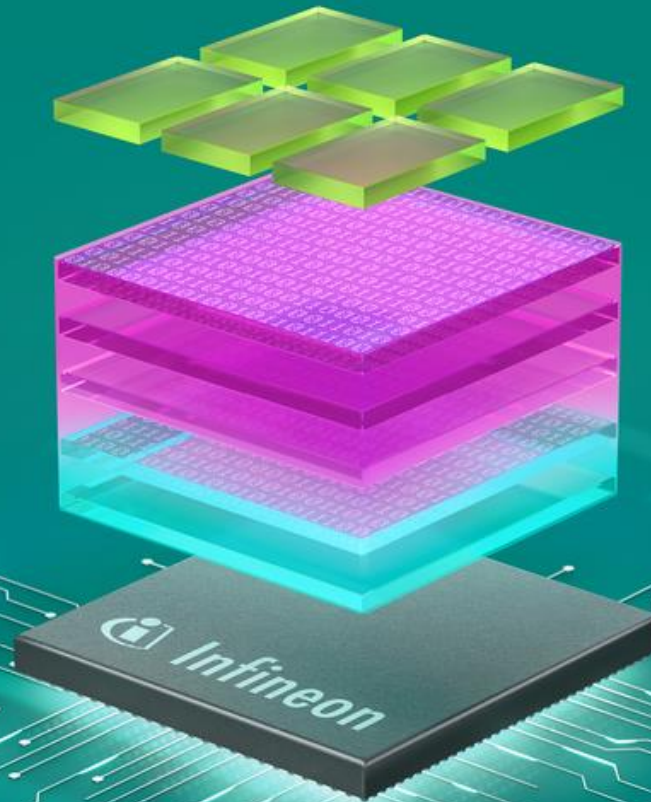


Accelerating and simplifying automotive software development

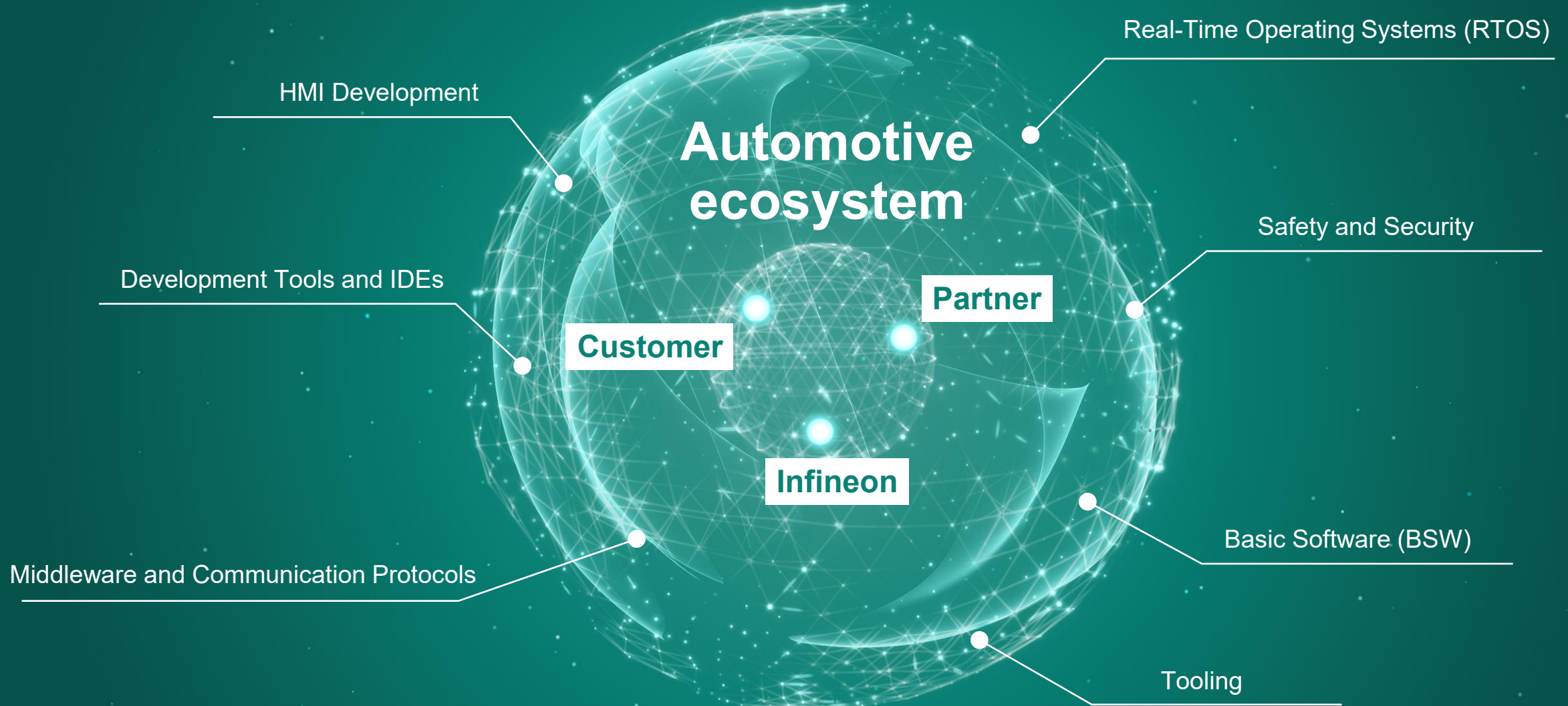
Drive Core

Software bundle overview

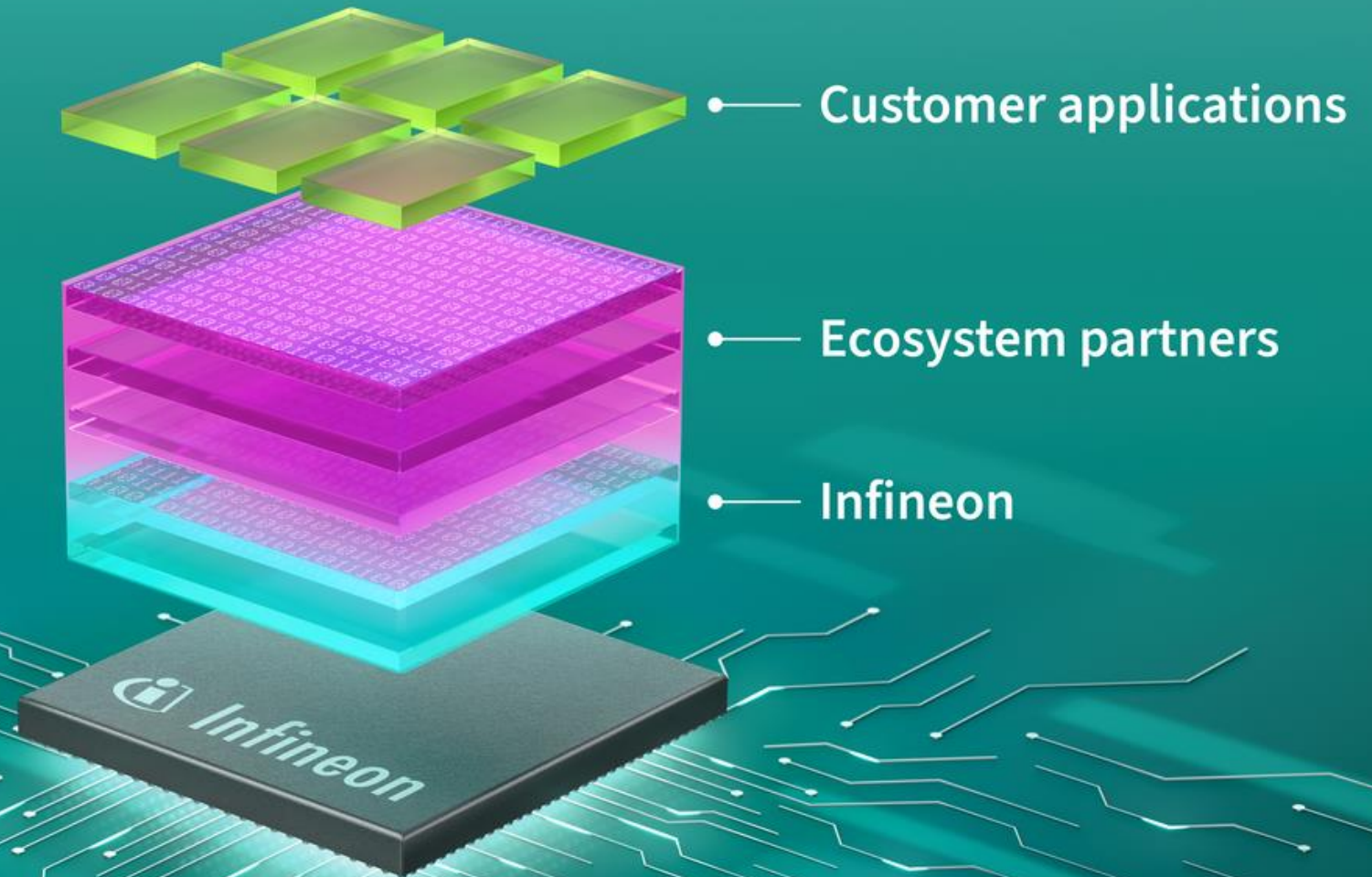
Lisa Kamp
Sep 2025



Ecosystem partnerships for accelerated software development



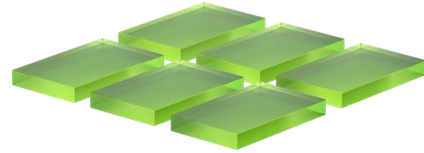
Drive Core



Drive Core is Infineon's scalable software portfolio to jumpstart software development for our Automotive Microcontrollers

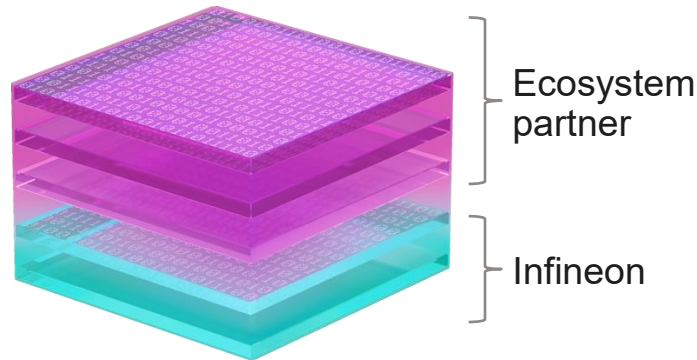


 **Customer Applications**

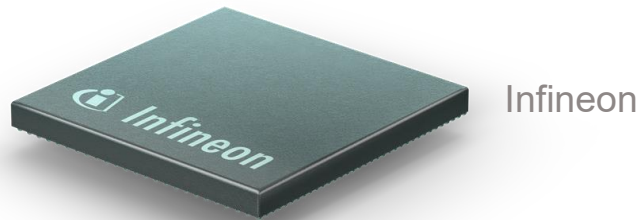


Drive Core

-  **Professional Tooling**
-  **Operating system
Middleware
Communication**
-  **Code examples
Hardware abstraction
(complex) drivers**



 **Microcontroller / Board**
PSOC™ AURIX™ TRAVEO™



Objectives

- Accelerate and simplify software development and reduce migration efforts on AURIX™, TRAVEO™ and PSOC™
- Reduce commercial complexity on supplier management

Benefits

- Pre-integrated Infineon and partner software & tools
- 3 months free evaluation for all components included
- Addressing major customer use cases and development flows incl. tool chain, embedded software, code examples and Infineon Board
- Getting started guide and code examples included

Committed partners



Drive Core for AURIX™

	AURIX™ TC4x	AURIX™ TC4x	AURIX™ TC4x	AURIX™ TC4x
	Drive Core AUTOSAR	Drive Core AI	Drive Core for NeuSAR	Drive Core AUTOSAR SB2 (Rust)
Compiler/Debugger	■ Tasking C/C++ Compiler/Debugger	■ HighTec C/C++ Compiler ■ Metaware PPU Compiler	■ HighTec C/C++ Compiler/Debugger	■ HighTec Rust and C/C++ Compiler
Tooling	■ Vector DaVinci Configurator ■ Tasking SmartCode IDE ■ EB Tresos ■ AURIX™ Flasher	■ MATLAB Simulink <i>(Pre-requisite)</i> ■ Synopsys Metaware IDE	■ NeuSAR Configurator ■ HighTec IDE ■ EB Tresos ■ AURIX™ Flasher	■ Tresos Studio ■ AURIX™ Flasher
Software	■ Vector MICROSAR Classic ■ Infineon MCAL	■ MathWorks HSP ■ TC4D iLLD MathWorks HSP	■ NeuSAR cCORE BSW ■ Infineon MCAL	■ EB Tresos AutoCore (AUTOSAR BSW) ■ Infineon MCAL
Hardware	■ KIT_TC4D9XP_COM_TRB	■ KIT_TC4D9XP_COM_TRB	■ KIT_TC4D9XP_COM_TRB	■ KIT_TC4D9XP_COM_TRB
Licensing	– Evaluation License	– Evaluation License	– Evaluation License	– Evaluation License
Quick start	– Getting Started Guide – Code examples – ACC Demo	– Getting Started Guide – Code examples – PPU tutorial – Signal classifier example	– Getting Started Guide – Code examples – Blinky LED Demo	– Getting Started Guide – Code examples – SWC in Rust and C Demo
Partners	   			 

■ Partner ■ Infineon

Drive Core for PSOC™ and TRAVEO™

	PSOC™	TRAVEO™
	Drive Core Control	Drive Core Graphics
Compiler/Debugger	■ IAR Compiler/ Debugger	■ IAR compiler/ Debugger
Tooling	■ Platform IO	– Qt 2-wheeler GUI – Infineon AutoFlash Utility
Software	■ Vector MICROSAR IO ■ AutoPDL ■ microHAL	■ Qt Graphics Library – Infineon SDL ■ Infineon Graphics Driver
Hardware	– KIT_PSOC4-HVMS-64K_LITE	■ KIT_T2G_C-2D-4M_LITE ■ Evaluation License
Licensing	– Evaluation License – <i>Commerical planned Q4</i>	– Getting Started Guide
Quick start	– Code examples – Blinkly LED Demo – Service communication Demo	– Code examples – Qt two-wheeler HMI Demo
Partners		
   		

■ Partner ■ Infineon

Accelerating Automotive Innovation

Drive Core Architecture Gains Traction with Top OEM Customers



AURIX™ Drive Core AI successfully introduced at EU OEM

Background

- OEM "MCU evaluation team" was mandated to evaluate short control loops on TC4x using the PPU (OBC/DCDC/HV/LV)
- No experience on PPU architecture and toolchain

Benefit

- AURIX™ Drive Core AI enabled software engineers to develop and test performance requirements within days instead of weeks, significantly reducing development time
- No licensing and contract negotiation was needed between OEM and tools / SW vendors → no need to pull in purchasing



AURIX™ Drive Core AI implemented by Premium OEM

Background

- Software development team was looking for ASIL D solution to run fast control loops (FOC algorithm) on the PPU for main traction inverter
- OEM "Motor Control Team" evaluated FOC algorithms for motor control on TC4x PPU, aiming to migrate their existing model-based designs to the PPU

Benefit

- Utilized model-based design to quickly evaluate and validate their motor control algorithms, accelerating the development process and reducing the risk of errors
- By utilizing the PPU for resource management to offload complex calculations, freeing up system resources and enabling more efficient processing of motor control tasks

Support material

Direct links

Drive Core
Promo Page

– [Drive Core](#)

Product pages

– [Drive Core for AURIX](#)
– [Drive Core for TRAVEO T2G](#)
– [Drive Core for PSOC](#)

Product briefs

– [Drive Core AI](#)
– [Drive Core AUTOSAR](#)
– [Drive Core for NeuSAR](#)
– [Drive Core Graphics](#)
– [Drive Core Control](#)

