

Embedded Bootloaders



HCC has developed optimized bootloaders for many years and have a range of high quality solutions that will fit almost any scenario. The bootloaders have the ability to boot using Flash, USB or serial interfaces using minimal resources on the target controller. The benefits of using HCC Bootloaders include small footprint, high-speed operation, optimized performance and a high quality stable implementation that has been widely used in commercial applications. HCC Bootloaders are fail-safe and if the boot process is interrupted for any reason the system will still be able to fully recover. All bootloaders have the option of adding an advanced AES encryption module for secure data transfer and can be configured to support multiple complex interfaces.

Embedded Bootloaders



Serial

HCC's straightforward serial bootloader consists of a simple serial protocol, together with an appropriate flash-programming algorithm. This quick and reliable solution is sufficient for applications that do not require complex interface or device support. It can be used together with HCC's encryption module for applications that require secure data transfer.

USB Device

The USB bootloader loads a new code image from a host application over a USB interface. It uses a special version of HCC's EUSBD stack and a simple transfer protocol to minimize overhead. This gives the application developer several easy options for loading target programs over USB from a host. On the host side the developer has several options to manage downloads:

- · HCC's free sample application that works with libusb (freely available).
- HCC's sample C# or VB application whith generic HID interface on Windows.
- · Use another generic USB host interface such as Winusb from Microsoft.

USB Host

The USB Host bootloader reads a new code image from any USB drive and uses it to update the target device's firmware safely. It contains a special, read-only version of HCC's optimized small-footprint FAT file system, together with HCC's USB stack and Mass Storage class driver.

■ FAT

HCC's optimized small-footprint FAT file system is used as the basis of a simple bootloader. Typically it is installed as a fixed bootstub in the target which is never erased. A trigger mechanism checks an attached SD card (or other similar media) for a new code image. If available, the bootloader will then automatically program the image to the target device.

■ Fail-safety

All HCC Bootloaders are fail-safe and will fully recover in the event of an unexpected interruption, reset or power loss. The system is configured to operate automatically and requires no user intervention to recover or handle unexpected system events.



AES Encryption

An advanced AES encryption add-on is available for all HCC bootloaders. The module completely conceals the program image using the AES encryption module so that only the bootloader for which the code is destined can decode it. Manufacturers can freely distribute new code and upgrades with full confidence that it cannot be dis-assembled, copied or tampered with.

Hybrid Configuration

HCC's bootloader products are configured from a core library of robust software components. This means that as well as the single function bootloaders described, HCC can supply composite bootloaders to support multiple interfaces. HCC's consulting process can easily and quickly deliver a bootloader built from tested core components to meet any system requirements.

■ Broad Range of Target Processors & Tools

HCC's Bootloaders can operate efficiently on a broad range of target processors. Designed to be ported easily and quickly to new architectures, it is available with drivers for a range of leading embedded processors.

RTOS Abstractions

RTOS abstractions are available for the following systems: CMX RTX, eCOS, emBOS, EUROS, FreeRTOS, Keil RTX, Nucleus, Quadros RTXC, ThreadX, $\mu\text{-velOSity}, \mu\text{C}/\text{OS-II},$ and many others. Importantly, for custom schedulers and super loops, HCC offers an abstraction for 'No RTOS'. We also offer our own eTaskSync, a small cooperative scheduler, which is designed to handle all processing and interface requirements of HCC middleware. This means that developers can choose our robust quality and outstanding performance irrespective of their legacy software.



Extensive Compiler Support

Eclipse/GCC, IAR Embedded Workbench, Keil ARM Compiler, Freescale CodeWarrior, Atmel AVR Studio, Green Hills Multi, Microchip MPLAB, Renesas HEW, TI Code Composer Studio, Mentor CodeSourcery, Atollic True Studio and many more.

Microcontrollers

ARM Cortex-MO/M1/M3/M4/R4/A8, ARM7/9/11; **Atmel** AVR32, SAM3/4/7/9; **Freescale** ColdFire, Kinetis, PowerPC, i.MX, Vybrid, QorlQ; **Infineon** C164, XMC1000, XMC4000; **Microchip** PIC24, PIC32; **NXP** LPC1300/1700/1800/2000/3000/4000; **Renesas** SuperH, RX, RL, 78k; **SiliconLabs** EFM32, SIM3; **Spansion** FMO/FM3/FM4; **STMicroelectronics** STM32; **Texas Instruments** MSP430, Stellaris, C2000, Hercules, DaVinci, Sitara, Tiva; **Toshiba** TMP MO/M3;

Licensing & Purchasing

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