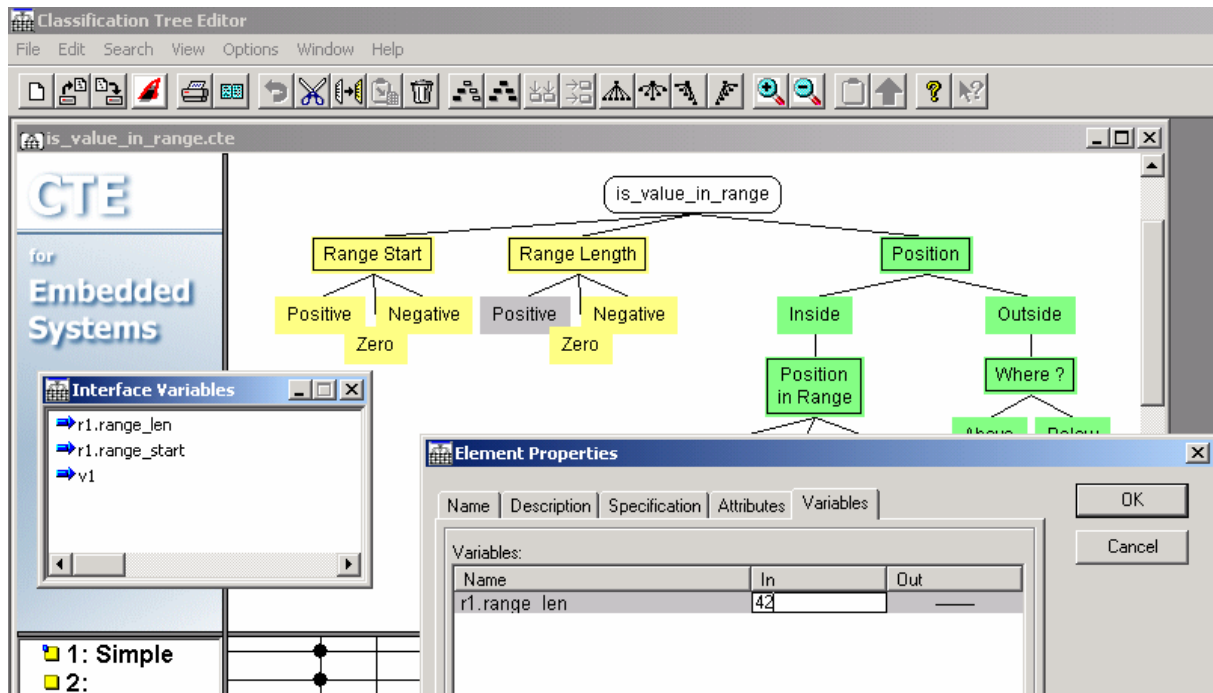


## Tessy V2.4 Features

### Variable and Value Assignments within CTE

The interface between Tessy and CTE has been extended to provide a list of input and output variables to CTE that may be assigned to elements (classifications and classes) of the classification tree. This allows to assign values for variables of the test object interface within the tree. If such a tree element is selected for a test case within the combination table, the respective value will be used for export to Tessy.



The values resulting from the markings of a test case or test step are automatically transferred into the test database of Tessy during the export operation. Subsequent export operations will update the values within the Tessy test database according to changes within the test cases or test steps.

### New Evaluation Mode with Deviation Value

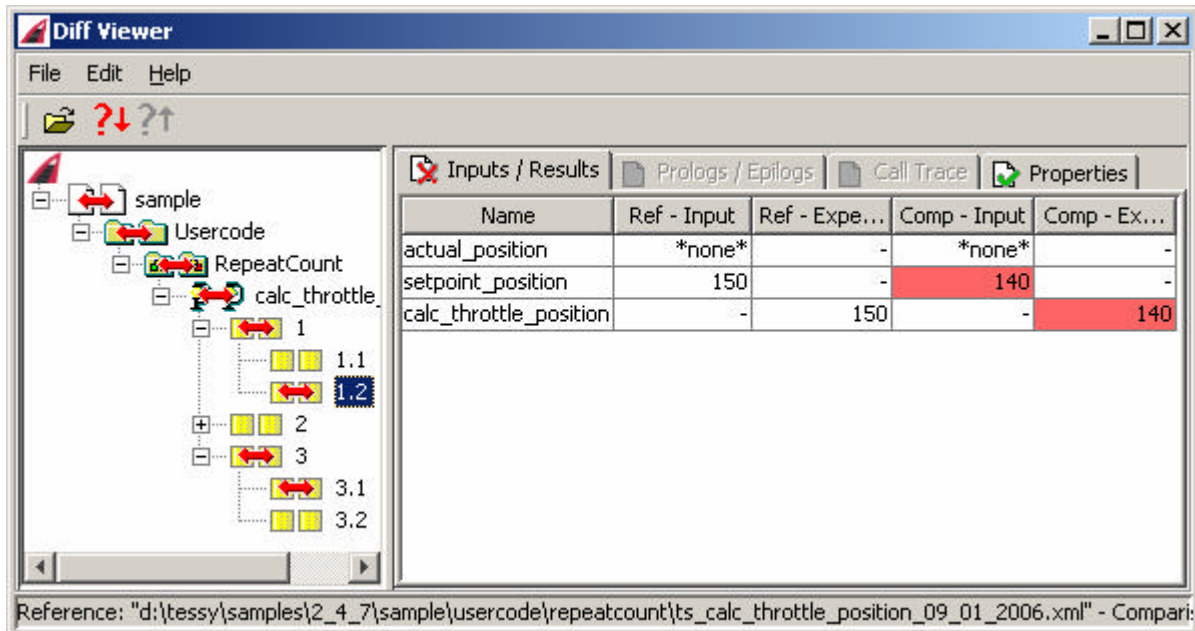
It is now possible to optionally provide a deviation value for each expected value when using the new evaluation mode "Delta". Within a separate input field, the deviation of the actual result compared to the expected result may be specified.



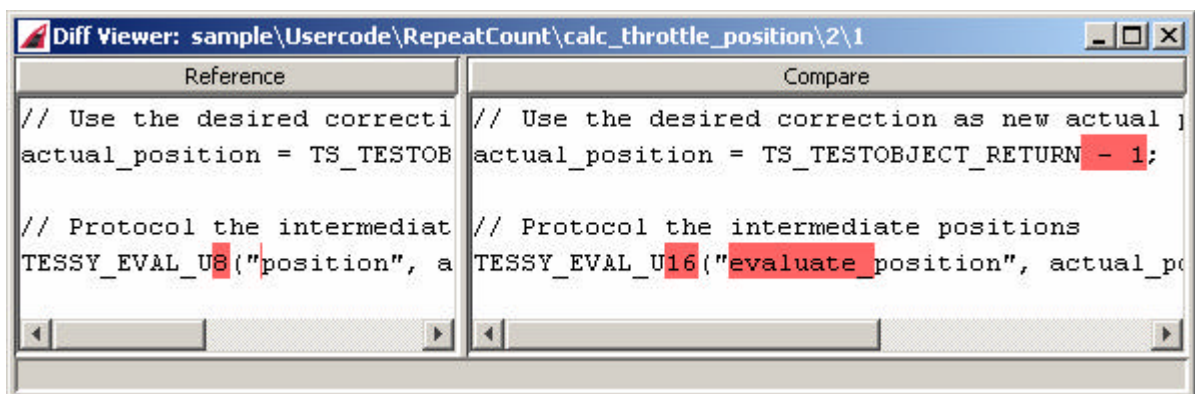
The deviation value may be provided as absolute number or as percentage of the expected result. If desired, the evaluation mode including the deviation value may be assigned to all test cases and test steps via the context menu.

### Visualization of Differences in Test Versions

A new tool allows visualizing the differences between two versions of the same module test (i.e. before and after a reuse operation). Test versions are stored within XML files and snapshots may be taken on user demand as well as automatically (i.e. before each reuse operation).



The tool may also visualize the differences between two XML report files, enabling later analysis of changes between the tests of two software versions. The tools shows both the differences in test data (input and expected values) and the differences within usercode (i.e. in prolog and epilog sections).



## Graphical Result Plots using MATLAB

The MATLAB integration allows to graphically visualizing the test results using MATLAB plots, either for data rows within one test step (e.g. arrays) as well as data rows resulting from one variable over all test steps. By selecting a special evaluation mode, you may choose the kind of result visualization by selecting one of the following result plots:

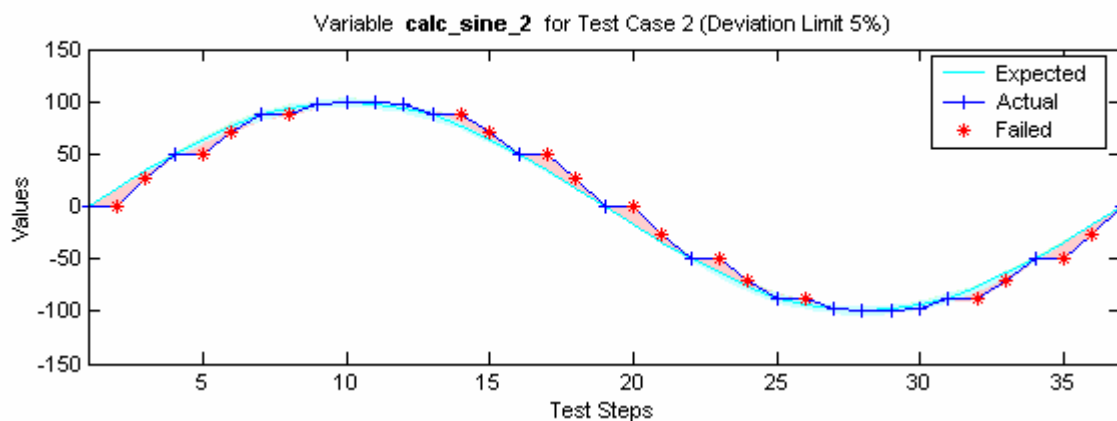
- result plot using the values of an array
- result plot of one variable over all test steps of each test case
- result plot of one variable over all test cases

It is further possible to specify a deviation value for the evaluation as described above.



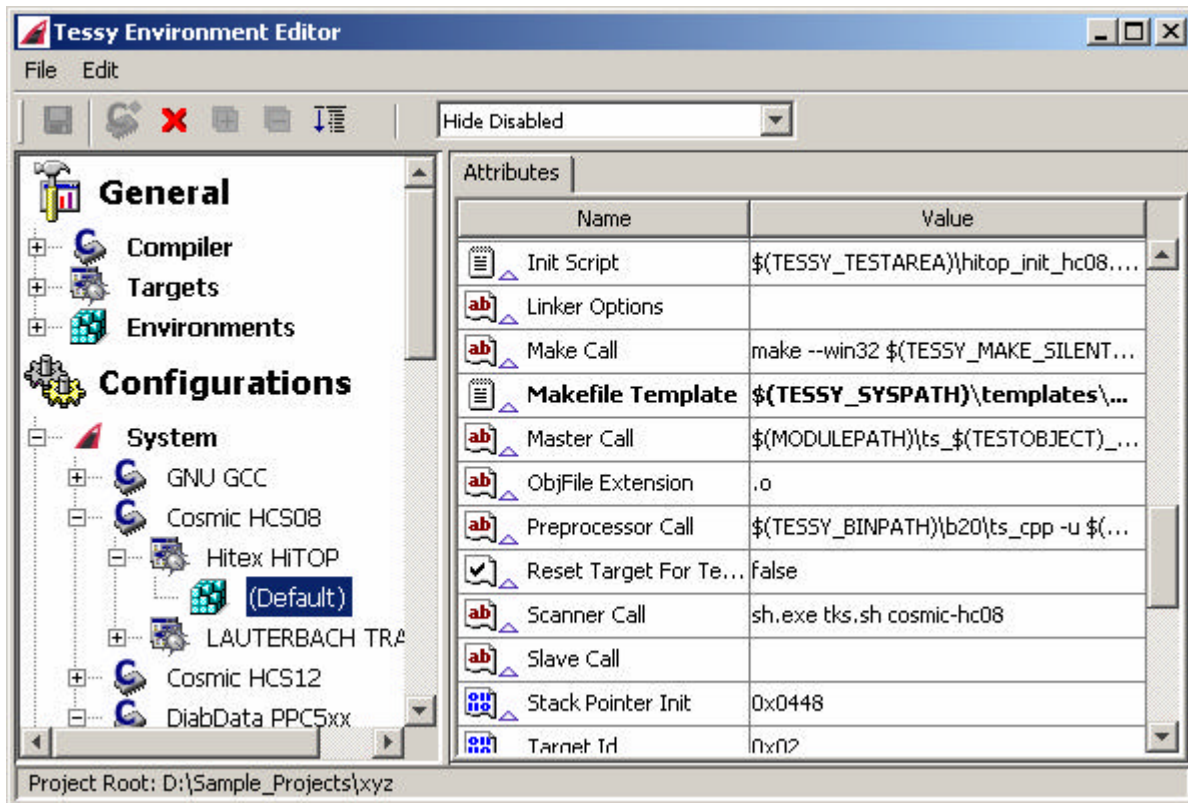
The result plots will be generated using MATLAB functions (this feature requires MATLAB to be installed). The result plots will look like shown below.

### Testcase MATLAB Plot:



## Enhanced Target and Compiler Configuration Management

Setting up new target environments and compilers is now much easier. The configuration editor allows to change specific settings locally or for whole development groups (e.g. on a network/"all user" level). Configurations can be imported and exported. This allows easy sharing of specific settings between developers.



## Enhanced Interface Analysis

Source files and the respective header files will be scanned and a list of dependencies will be generated. This allows to check for file changes when opening modules before analyzing the interface.

If no file changes have occurred, no comparison between old and new interface will be carried out, thus saving time when opening the module.

## New Licensing Model

Starting with V2.4, TESSY is available as floating license (as it is currently available) and as node-locked license (new).

### ***Additional Microcontroller Architectures and Compilers Supported***

<b>Tessy V2.4 supports additionally to Tessy V2.3 the following</b>	
<b><i>Microcontroller architectures</i></b>	
<b>Microcontroller manufacturer</b>	<b>Architecture</b>
Analog Devices	Blackfin DSP
Freescale	S12X
Fujitsu	FFMC32
Microchip	PIC18
Renesas	H8S
Texas Instruments	MSP430
<b><i>Compilers</i></b>	
<b>Compiler manufacturer</b>	<b>Architecture</b>
Analog Devices	Blackfin DSP
Hi-Tech	PIC18
IAR	H8S, PIC18, MSP430
Keil	ARM
Knudsen	PIC 18
NEC	NEC V850

Tessy supports debuggers from various manufacturers.